

ABSTRACT OF THE DISCLOSURE

A printed circuit board (PCB) is provided that maximizes compensation capacitance per unit area of the PCB while minimizing signal transmission delays in the PCB. The PCB includes a first section having a first dielectric constant (DK), a second section having a second DK lower than the first DK and provided above or below the first section, a plurality of crosstalk compensation elements provided in the first section, and a plurality of circuit elements provided in the second section.